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Docket No.: Z&P-INF-P10709

Date: July 25, 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Enclosed herewith are the necessary papers for filing the following application for Letters Patent:

**Applicant** 

DIETRICH BONART ET AL.

Title

METHOD FOR FABRICATING A VERTICAL TRANSISTOR, AND

SEMICONDUCTOR MEMORY CELL HAVING A TRENCH CAPACITOR AND AN ASSOCIATED VERTICAL SELECTION

TRANSISTOR

6 sheets of drawings.

The payment in the amount of \$768.00 covering the filing fee. Claim for Priority. Certified copy of the corresponding foreign patent application No. 102 33 916.3 filed July 25, 2002.

Information Disclosure Statement and 6 References.

This application is being filed without a signed oath or declaration under the provisions of 37 CFR 1.53(f). Applicants await notification of the date by which the oath or declaration and the surcharge are due, pursuant to this rule.

The Patent and Trademark Office is hereby given authority to charge Deposit Account No. 12-1099 of Lerner and Greenberg, P.A. for any fees due or deficiencies of payments made for any purpose during the pendency of the above-identified application.

Respectfully submitted

LAURENCE A. GREENBERG REG. NO. 29,308

For Applicants

L∕AG:kf